

AMENDMENT TO THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) A method for producing a bonded wafer, comprising:
growing a silicon epitaxial layer comprising boron having a concentration of 5×10^{18} atoms/cm³ or greater 8×10^{18} atoms/cm³ to 2×10^{19} atoms/cm³ on an active layer silicon wafer;
forming an insulating film in a surface of said silicon epitaxial layer;
following said insulating film formation, ion-implanting a light element into said silicon epitaxial layer at a predetermined depth to thereby form an ion-implanted area therein;
following said ion implantation, bonding said active layer silicon wafer and a supporting wafer together with said insulating film interposed therebetween to thereby form a bonded wafer;
heat treating said bonded wafer to cause bubbles of light element to be generated in said ion-implanted area and thereby a part of said active layer silicon wafer to be cleaved and separated at the site of said predetermined depth for forming an active layer; and
after said insulating layer formation on said active layer silicon wafer or said cleavage of the part of said active layer silicon wafer, performing an annealing treatment on said active layer silicon wafer or bonded wafer at a temperature of $1,000^{\circ}\text{C}$ or higher $1,100^{\circ}\text{C}$ to $1,200^{\circ}\text{C}$ and for a duration of one hour or longer to four hours in a reducing gas atmosphere comprising hydrogen gas.

2. (Canceled)

3. (Currently Amended) A method for producing a bonded wafer in accordance with claim 1, in which a thickness of said silicon epitaxial layer is ~~0.3 μ m or thicker~~ 0.5 μ m to 1.5 μ m.

4. (Canceled)

5. (Previously Presented) A method for producing a bonded wafer in accordance with claim 1, in which a thickness of said insulating film is thinner than 0.2 μ m.

6. (Withdrawn – Previously Presented) A method for producing a bonded wafer, comprising:

an ion-implantation step for ion-implanting a light element into a wafer for active layer at a predetermined depth to thereby form an ion-implanted area therein, said active layer wafer comprising an insulating film formed thereon and containing boron at a concentration of 9×10^{18} atoms/cm³ or higher and oxygen at a concentration below 12×10^{17} atoms/cm³ (old ASTM);

a bonding step for subsequently bonding said active layer wafer that has been subjected to the ion implantation and a supporting wafer together with said insulating film interposed therebetween to thereby form a bonded wafer; and

a cleavage and separation step for heat treating said bonded wafer to cause bubbles of light element to be generated in said ion-implanted area and thereby a part of said active layer wafer to be cleaved and separated at the site of said predetermined depth for forming an active layer.

7.-11. (Canceled)

12. (Previously Presented) A method for producing a bonded wafer in accordance with claim 3, in which a thickness of said insulating film is thinner than 0.2 μm .

13.-17. (Canceled).

18. (Withdrawn – Previously Presented) A method for producing a bonded wafer in accordance with claim 6, in which an annealing process is applied to said active layer wafer or said bonded wafer at 1000°C or a higher temperature for one hour or more in a reducing gas atmosphere containing hydrogen gas after said formation of said insulating film in said active layer wafer or said cleavage and separation of said active layer wafer.

19. (New) A method for producing a bonded wafer in accordance with claim 1, in which a thickness of said silicon epitaxial layer is 1 μm to 2 μm .